CDF Operations Summary

This is changing

Store	Start	Stop	hours	InitL	Deliv	LiveLumi	Good *	Silicon	Runs Taken During Store
3850 ☐	2004.12.16-00:11 Thurs.	2004.12.16-11:29	11.3	53.7	1,368.0	1,256.5 91.9%	0.0 0.0%	0.0 0.0%	191205 191208
3845 ☐	2004.12.14-11:08 Tues.	2004.12.15-06:25	19.3	17.6	800.3	579.8 72.4%	389.7 48.7%	0.0 0.0%	191071 191074 191078 191086 191087 191088 191089 191096 191098 191099 191100 191101 191102 191103 191104 191105 191106 191107 191109 191110 191111
3841 ☐	2004.12.13-10:54 Mon.	2004.12.13-11:08	0.2	52.5	45.7	28.4 62.1%	0.0 0.0%	0.0 0.0%	190990
3834 ☐	2004.12.11-02:57 Sat.	2004.12.12-06:50	27.9	56.6	2,570.8	1,837.6 71.5%	1,463.3 56.9%	0.0 0.0%	190915 190919 190920 190921 190922 190923 190925 190926 190928 190929 190930 190931 190932 190933 190934 190936
3829 ☐	2004.12.09-21:05 Thurs.	2004.12.10-11:19	14.2	50.8	1,587.7	1,160.0 73.1%	1,118.0 70.4%	0.0 0.0%	190851 190852 190853 190854 190855 190856 190857 190858 190859 190860 190861 190862 190863 190864 190865 190866
3824 ☐	2004.12.08-14:46 Wed.	2004.12.09-00:14	9.5	48.5	822.2	575.2 70.0%	560.4 68.2%	0.0 0.0%	190753 190754 190755 190756 190757 190758 190759 190760 190761 190762 190763 190764 190765
3821 ☐	2004.12.07-15:22 Tues.	2004.12.07-18:42	3.3	10.3	102.6	72.2 70.3%	20.7 20.2%	0.0 0.0%	190697 190698 190699 190700 190702 190703

CDF Operations Summary

- Thurday, Dec. 9:
  - Access in morning to fix several problems: central SMX crates, central CES HV, a couple of PMT problems, COT TDCs
  - RF1 fan shaft replaced
  - SVX D-mode calibrations
- Run 3829, Thursday, Dec. 9, 9:22 PM:
  - Continue 3-track board tests with beam; trigger table tests with tighter cuts
  - L2 decision board swapped out due to errors
- Run 3834, Saturday, Dec. 11, 2:56 AM:
  - Rough startup
    - *Loss blowup after scraping holds off running for one hour for rescrape*
    - *DAQErrorMon (new monitor) says COTD missing, but hits there (cost us one hour)*
  - Smooth running afterward; Sunday owl shift 99.8% efficient

CDF Operations Summary

- Run 3841, Monday, Dec. 13, 11:53 AM:
  - Hoping to try silicon after two hours of smooth operation but…
- Run 3845, Tuesday, Dec. 14, 11:08 AM:
  - Trigger tests:
    - *Test with DSP code and new COTD format*
    - *Test with increased CSL pointer space (more L2/L3 trigger bits)*
  - CSL problems–had to back up several versions
- Run 3850, Thursday, Dec. 16, 00:11 AM:
  - Dynamic prescaling working after bug fixes
  - Silicon tests:
    - *Use old trigger table (just as sanity check))*
    - *Use current trigger table*
    - *SVT tests*
  - Try running with new DSP code for extended period
  - Consumers–all are 6.0.0int3 now except SVXMON